

FIG. 1

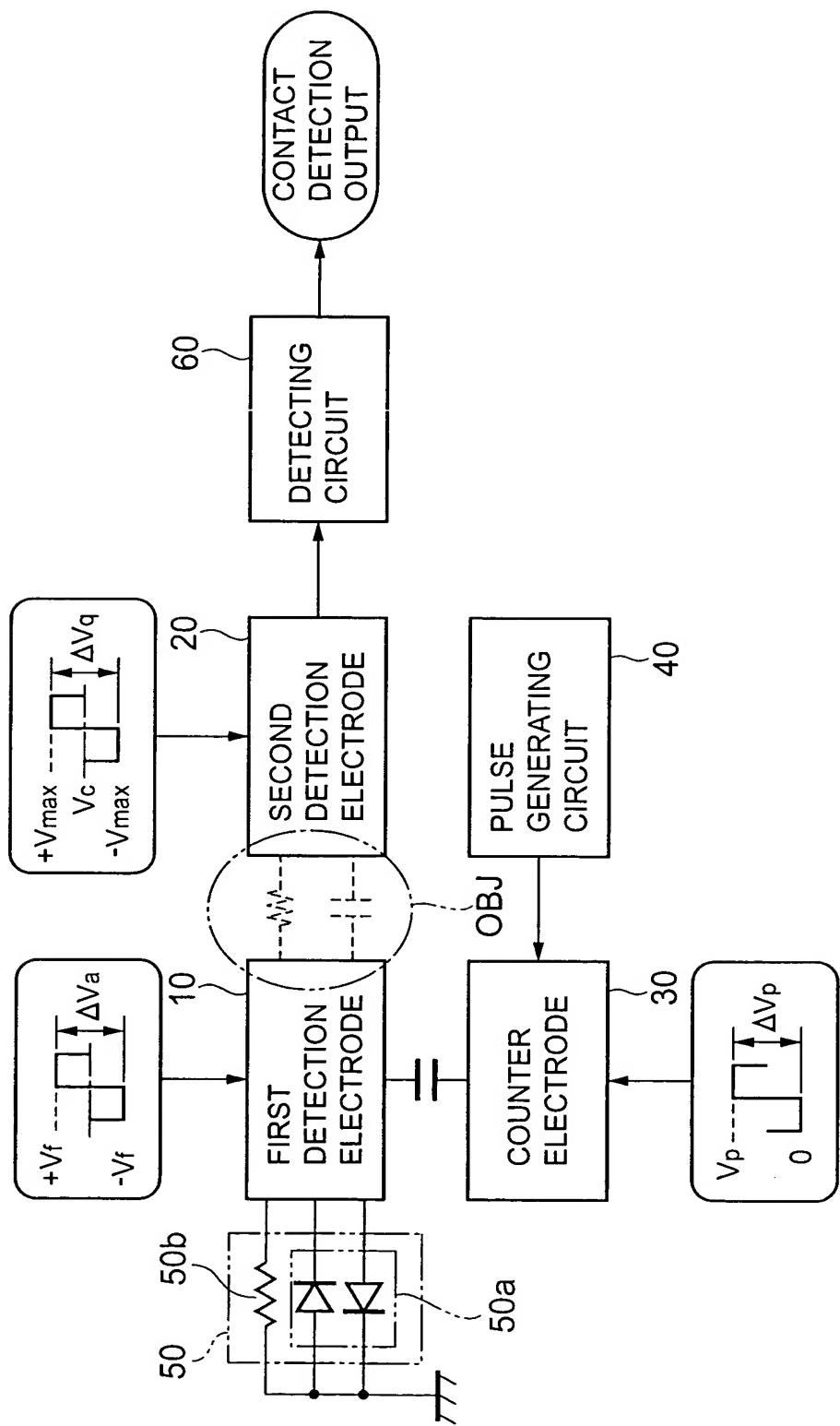


FIG.2

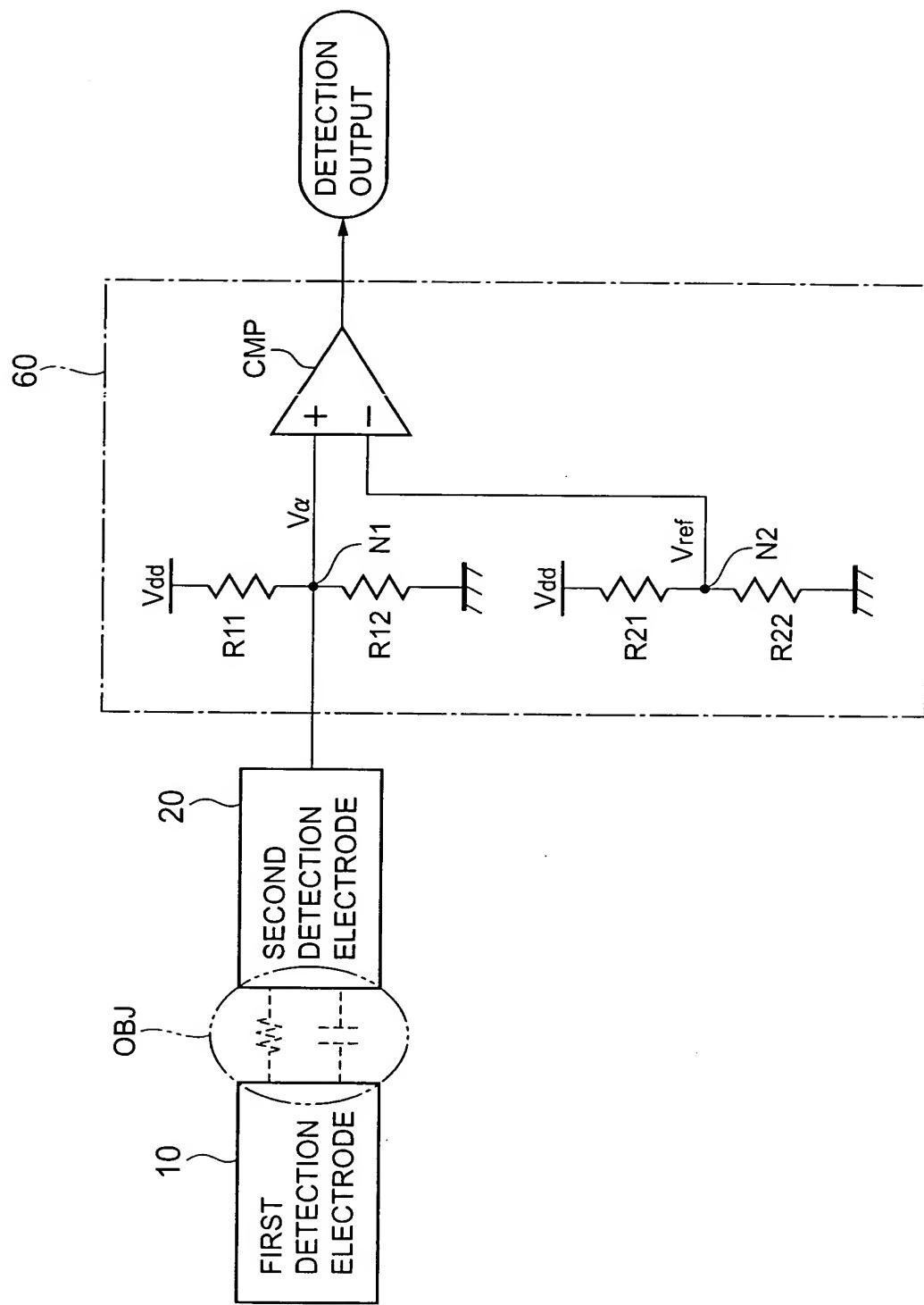


FIG.3A

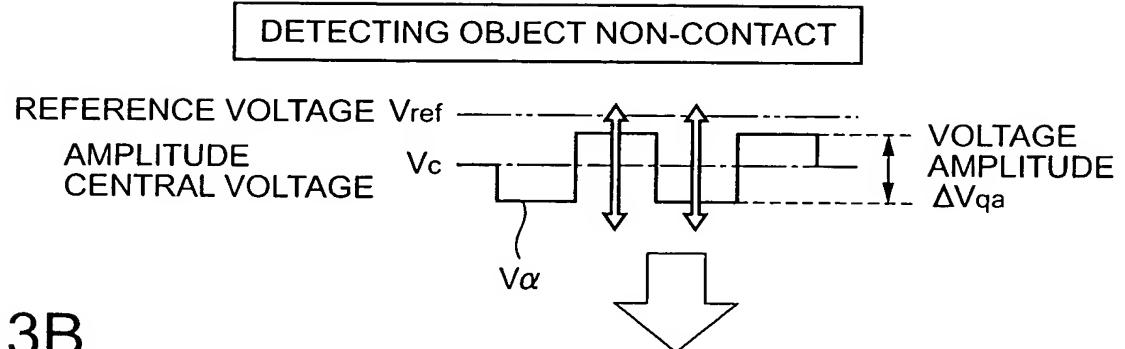


FIG.3B

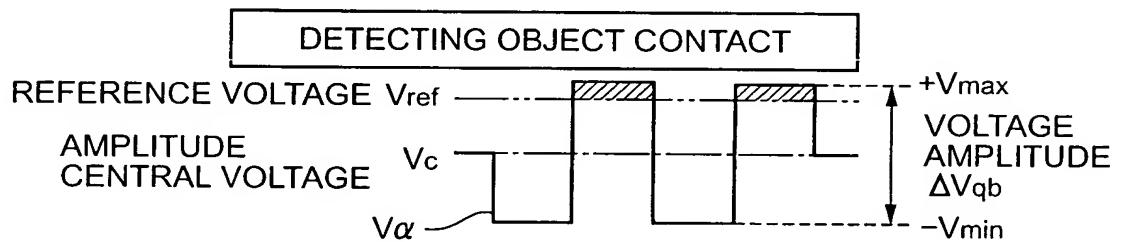


FIG.3C

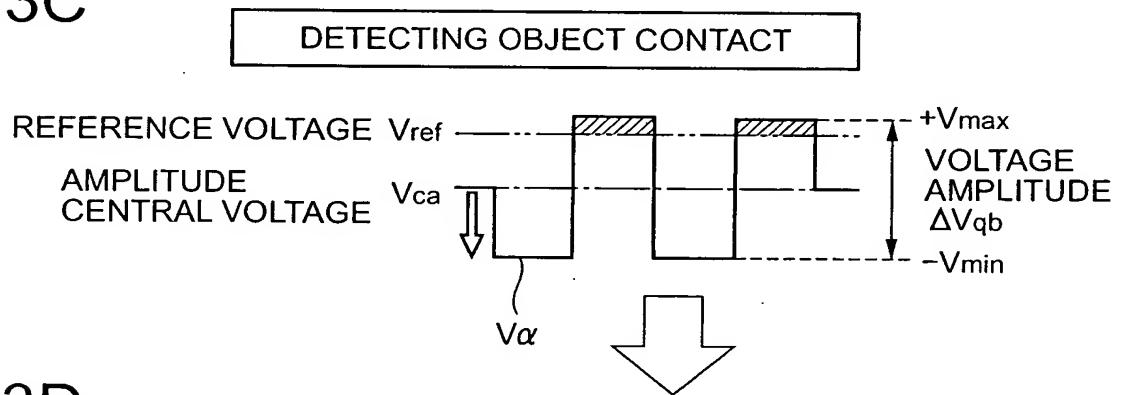


FIG.3D

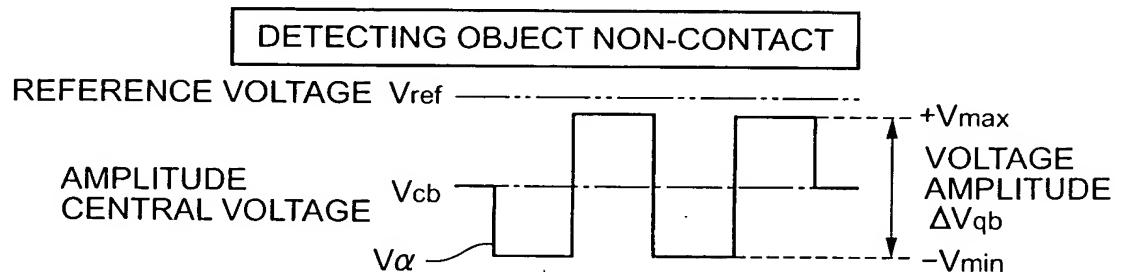


FIG.4A

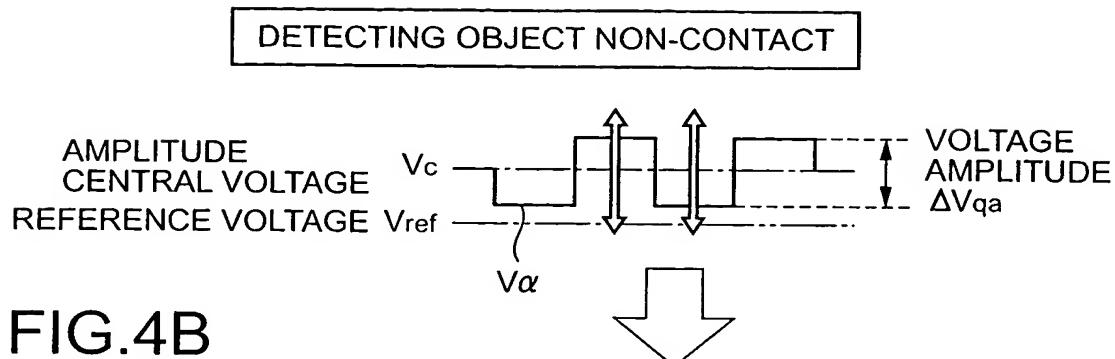


FIG.4B

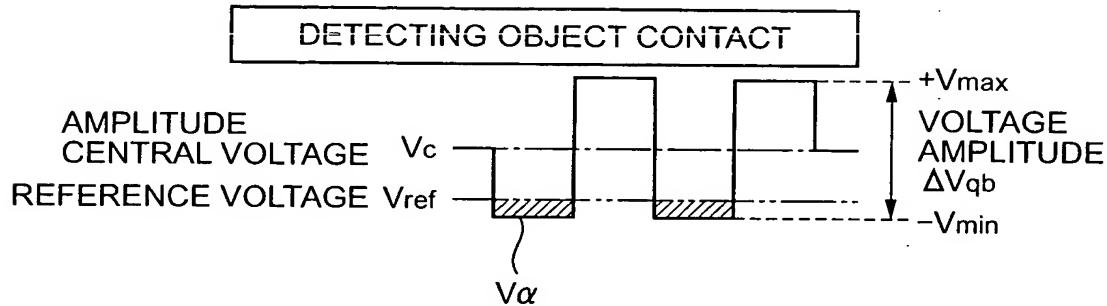


FIG.4C

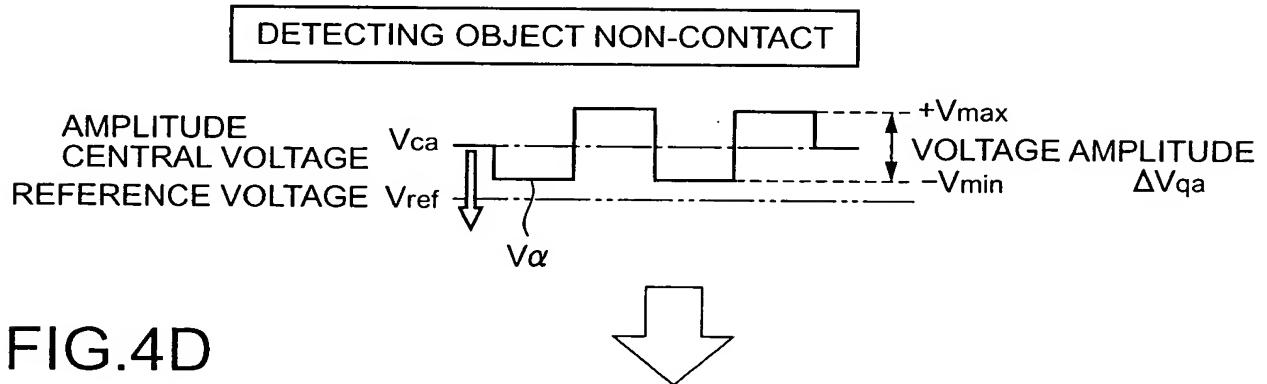


FIG.4D

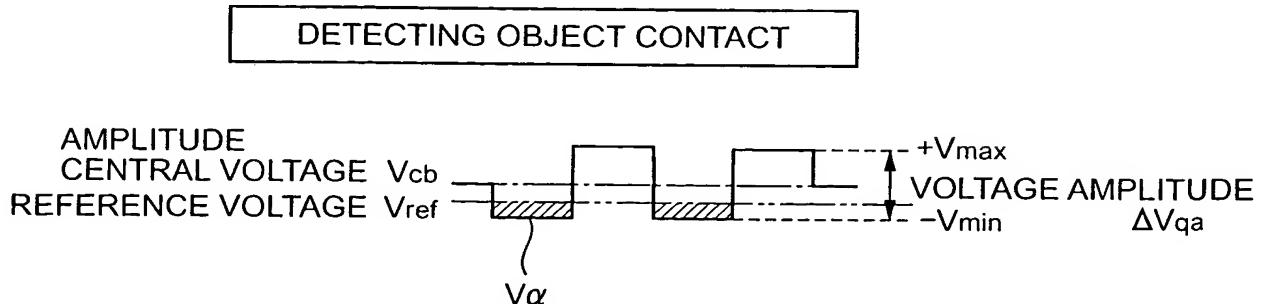


FIG.5

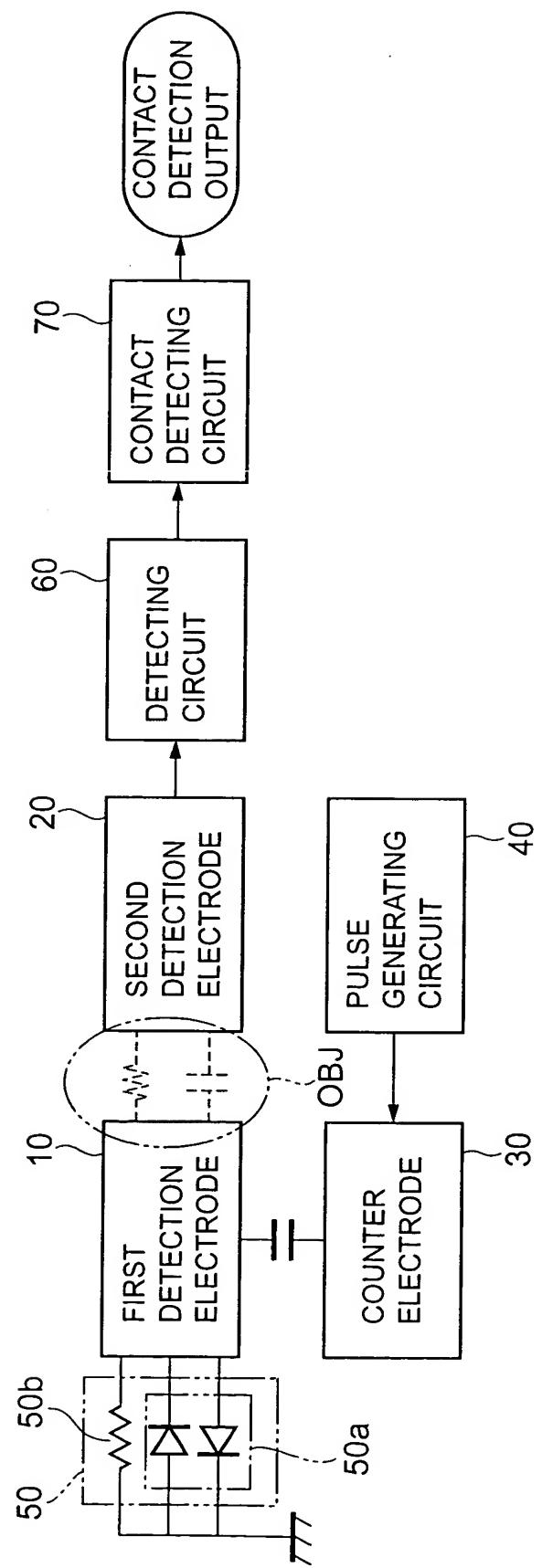
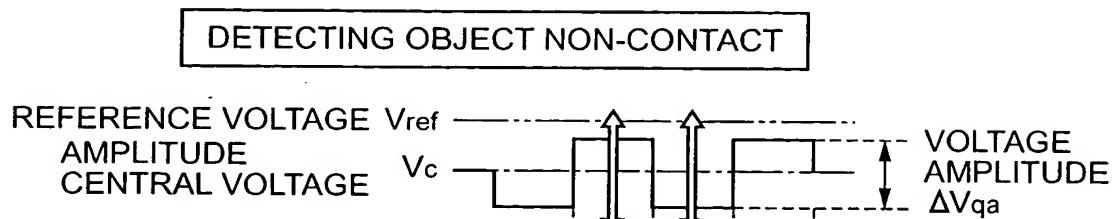
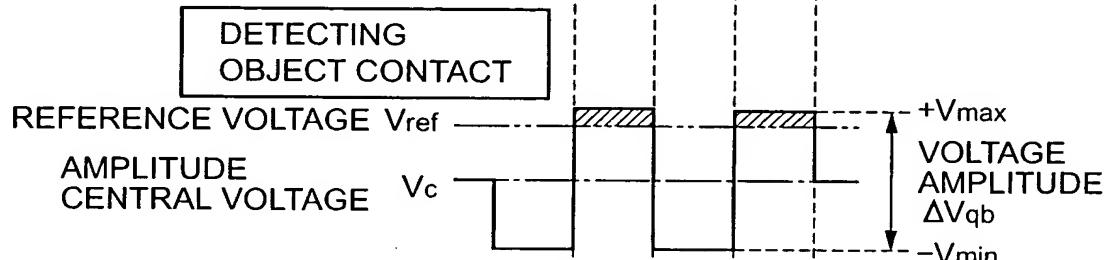


FIG.6A**FIG.6B****FIG.6C**

OUTPUT SIGNAL OF
DETECTING CIRCUIT

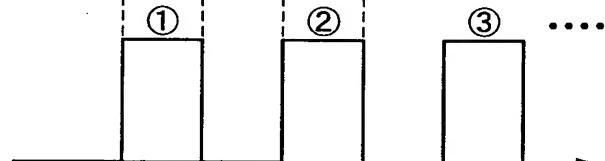


FIG.7A

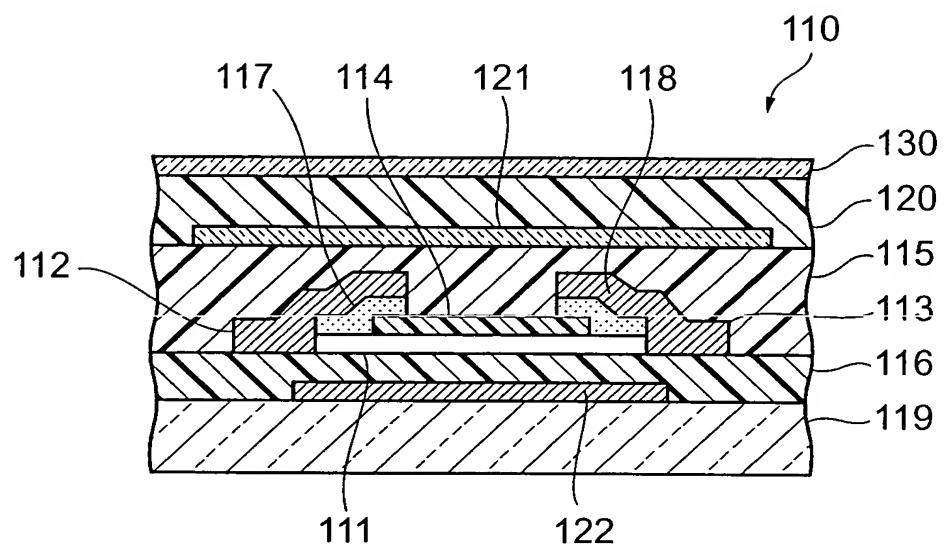


FIG.7B

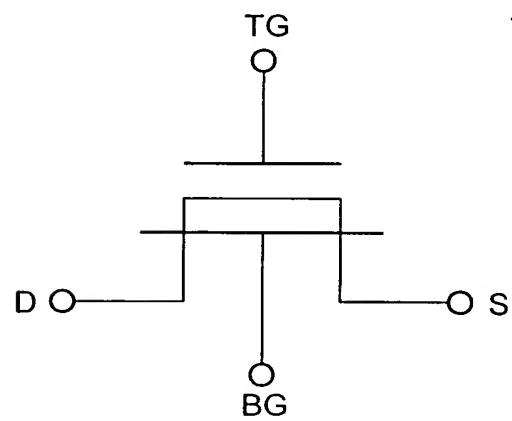


FIG.8

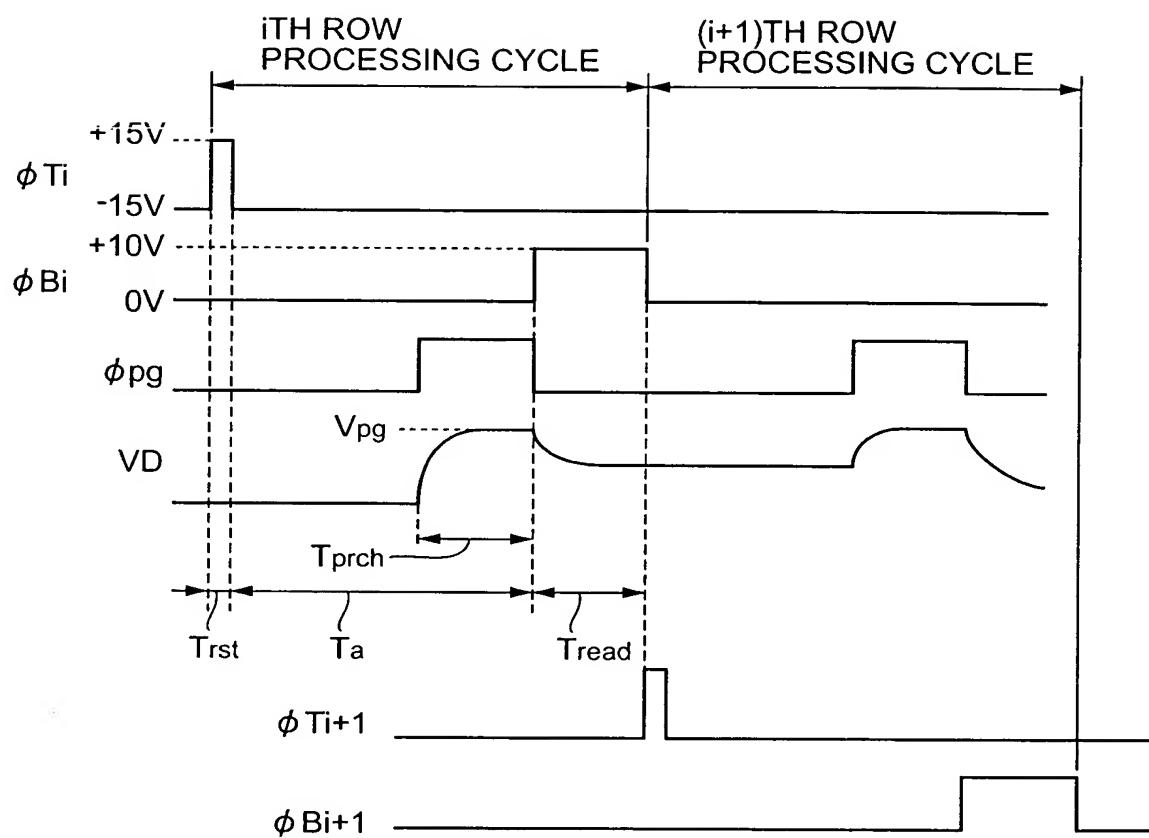


FIG.9

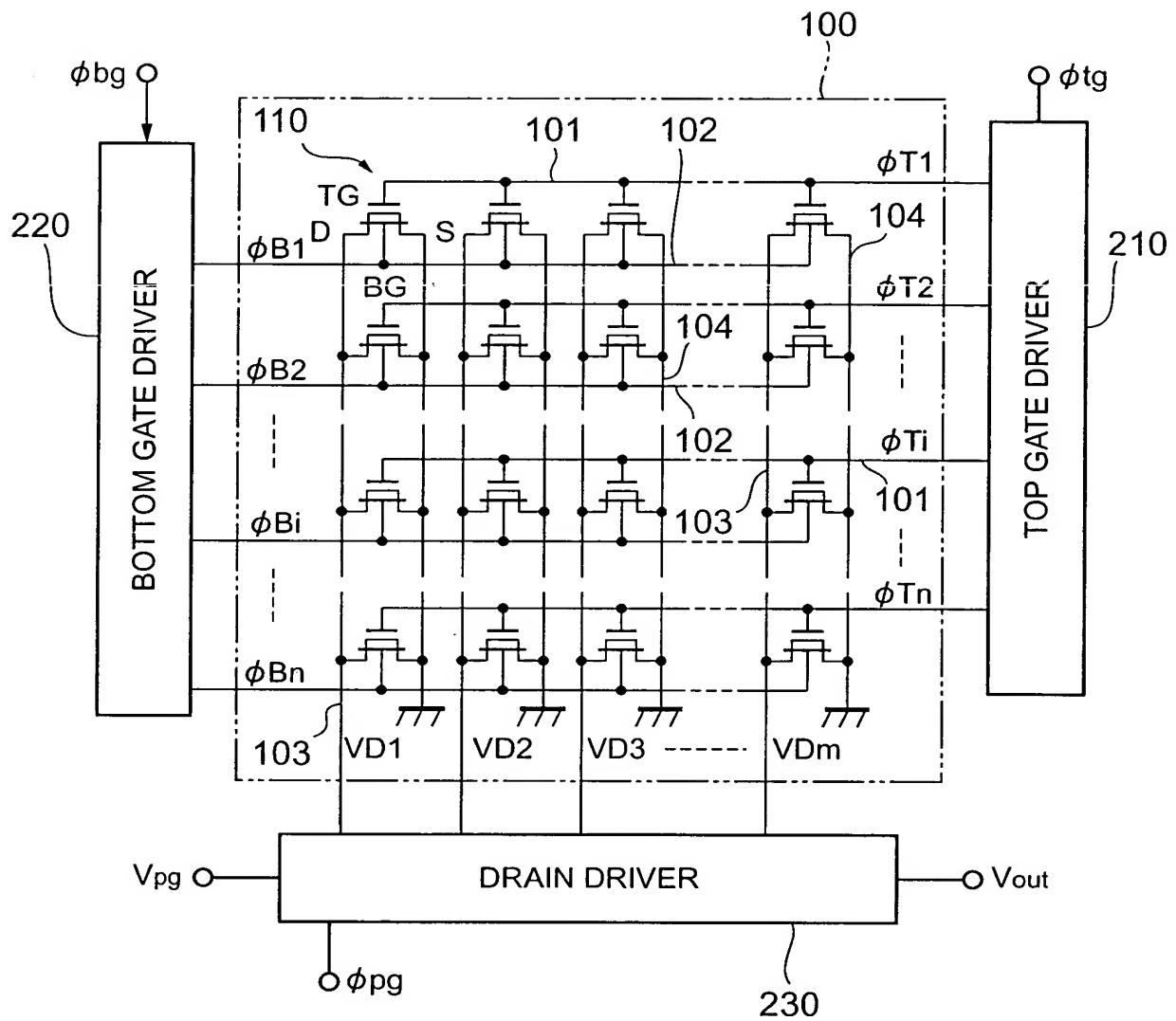


FIG.10

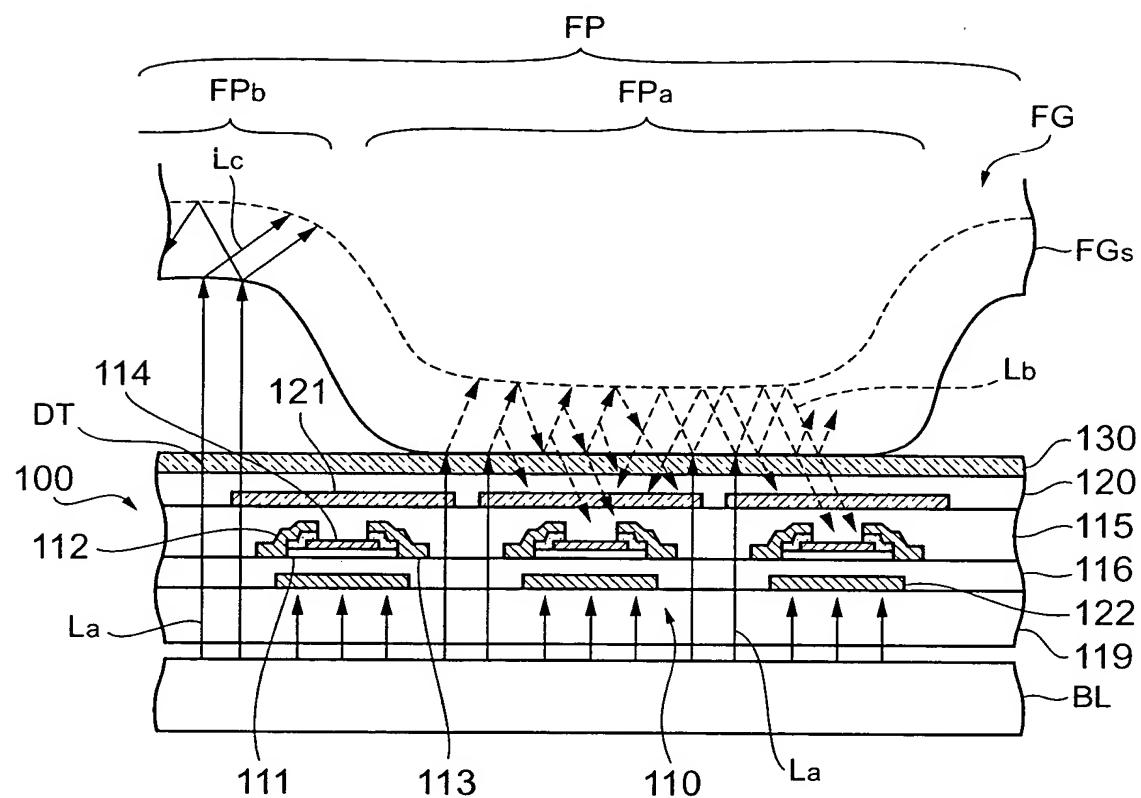


FIG.11A

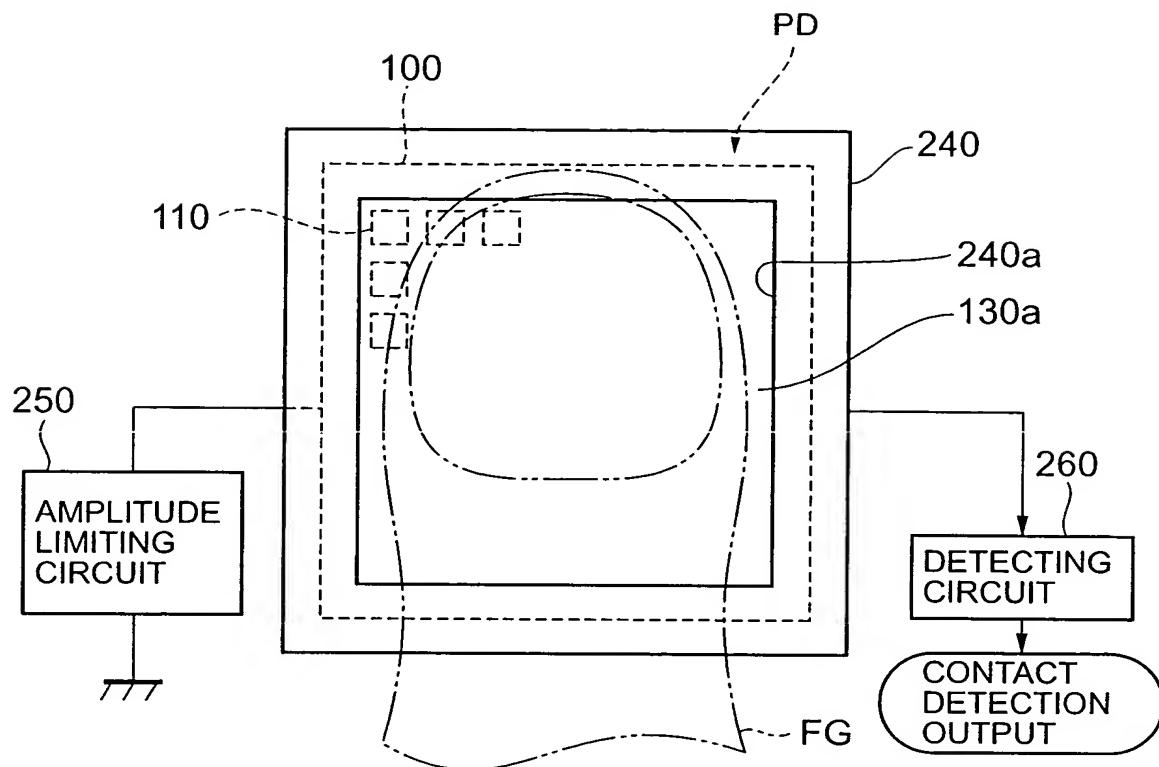


FIG.11B

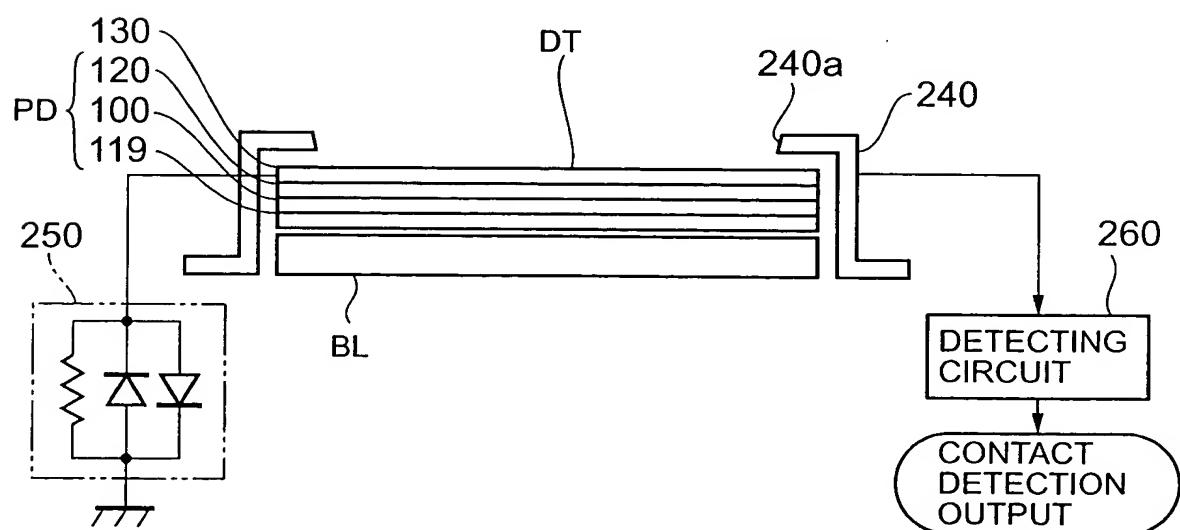


FIG.12A

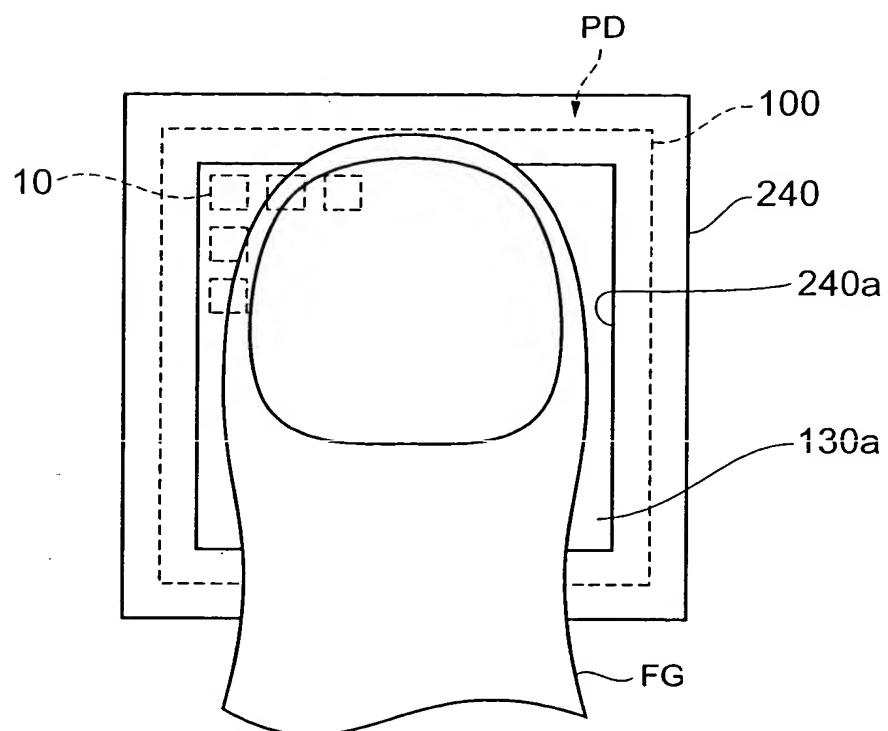


FIG.12B

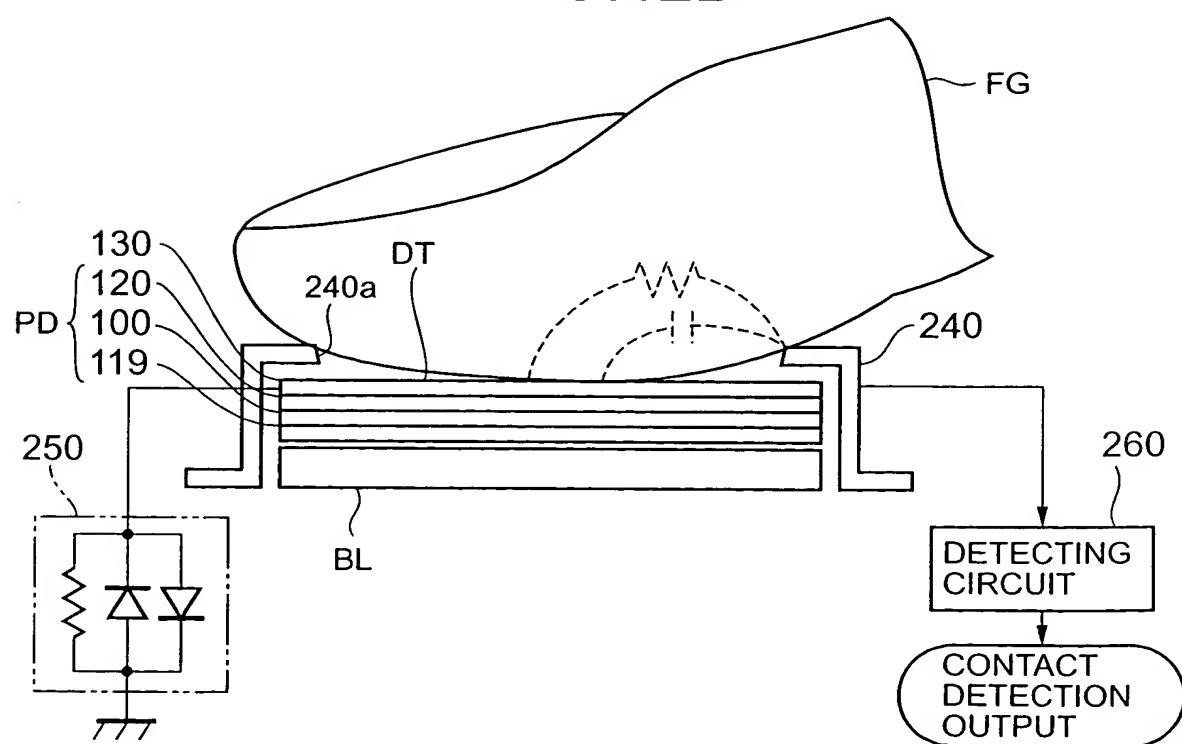


FIG.13A

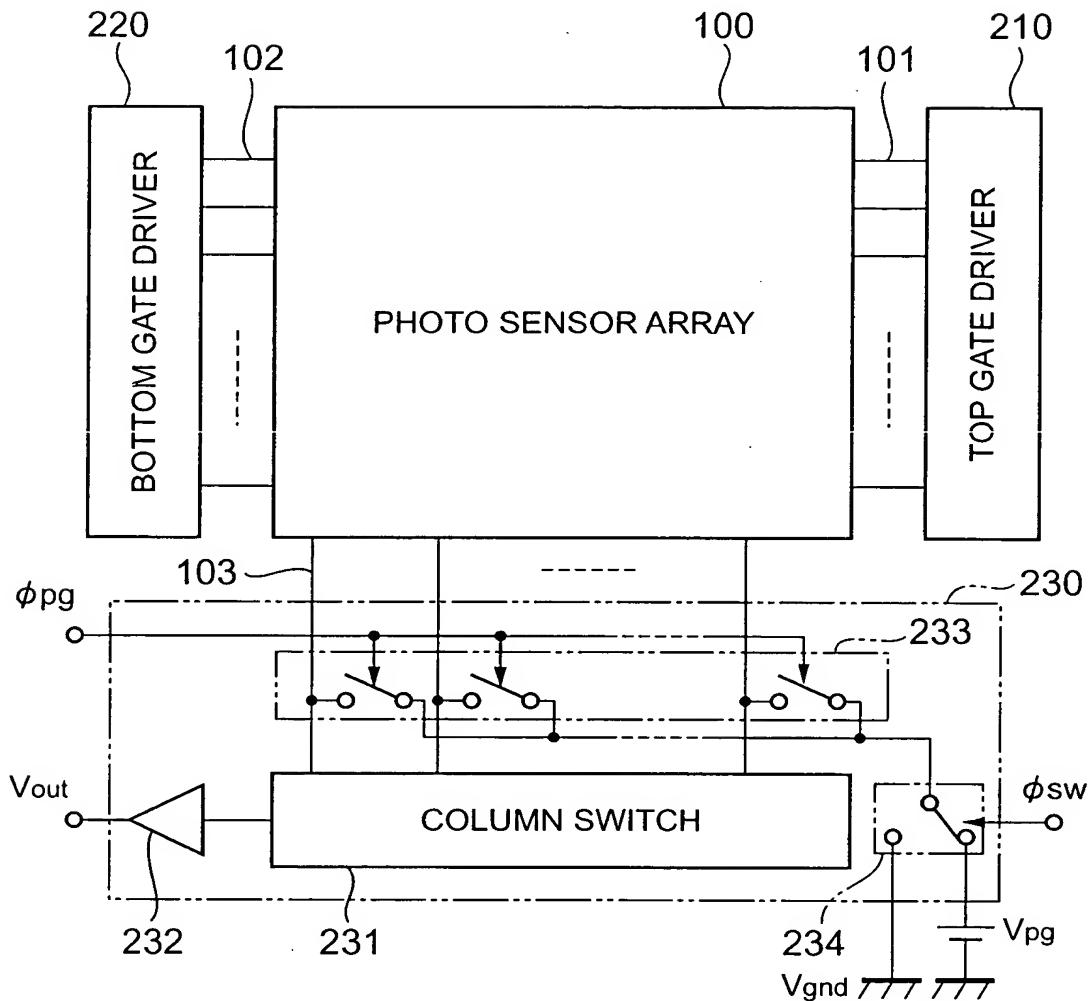


FIG.13B

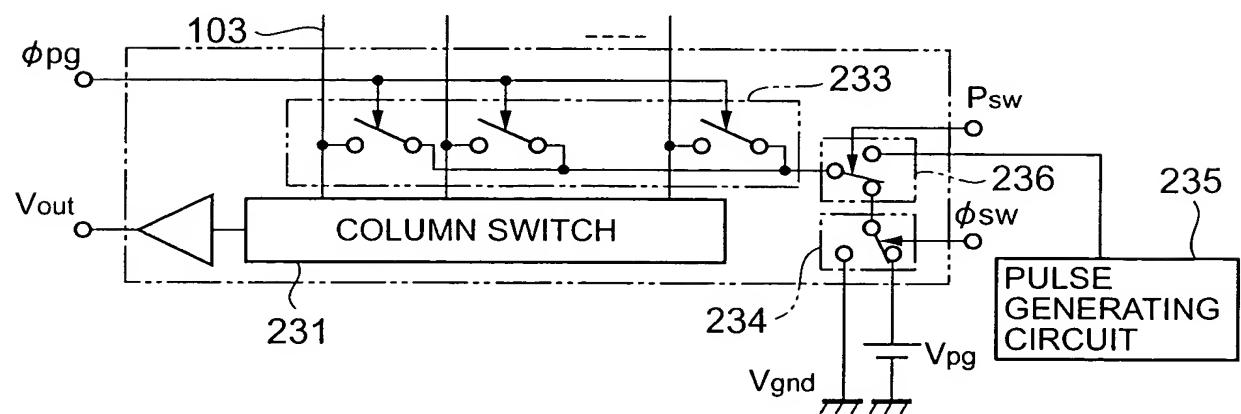


FIG.14

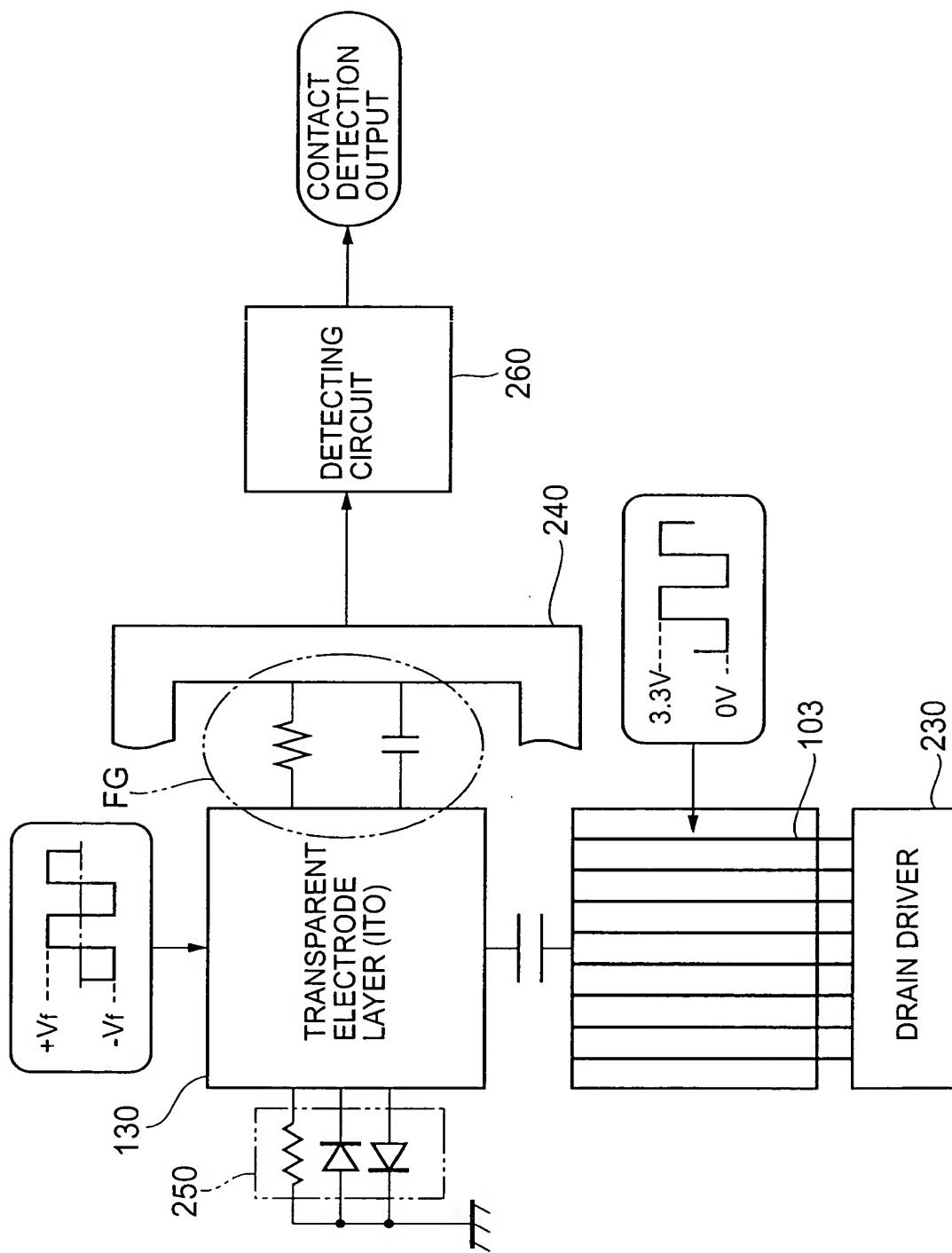


FIG. 15

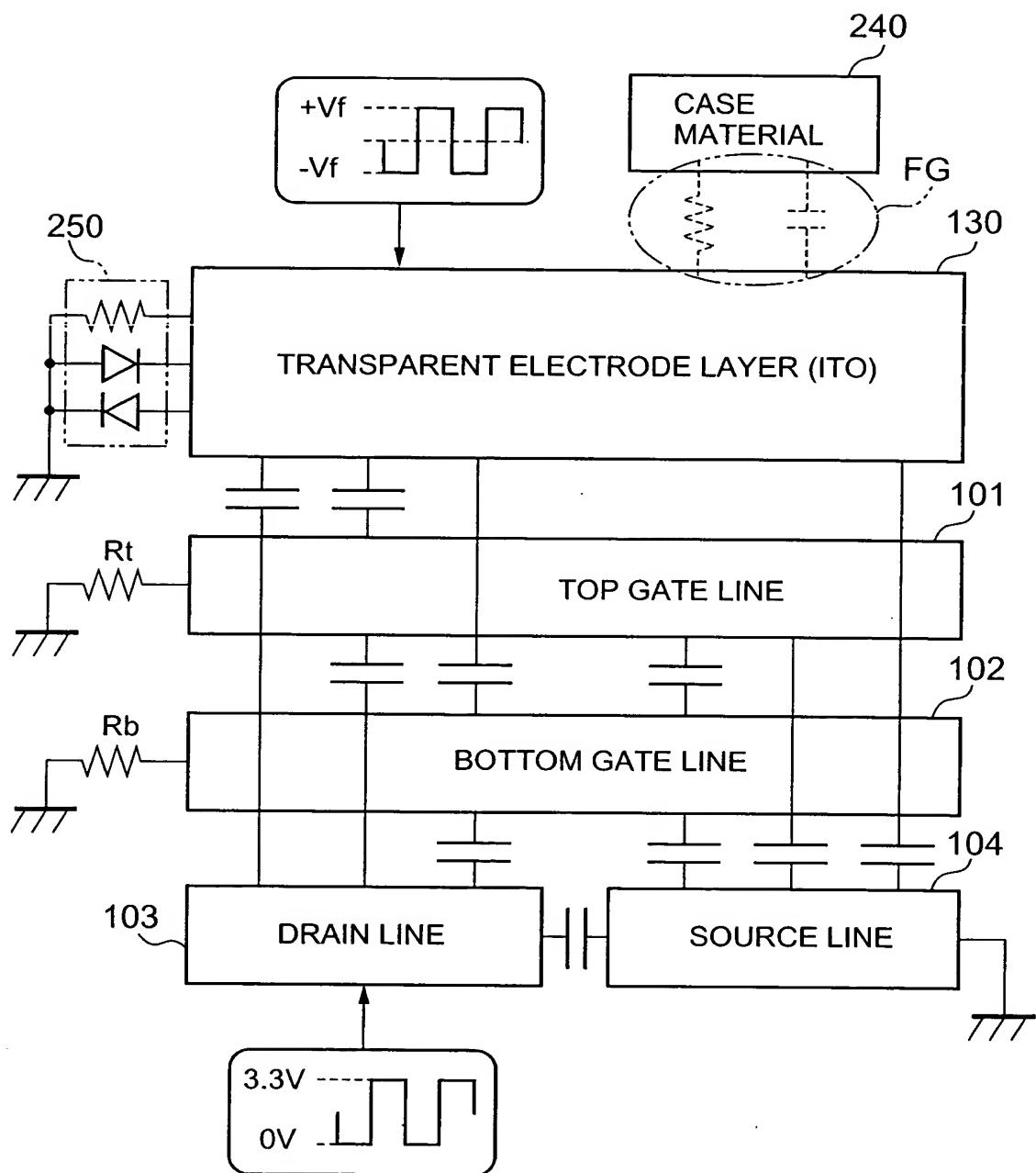


FIG. 16A

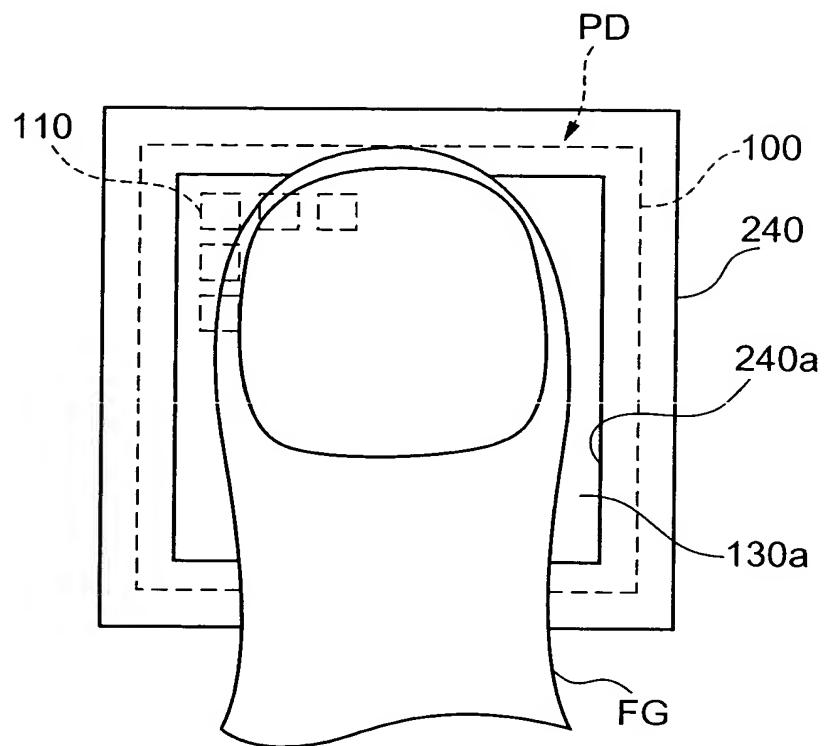


FIG. 16B

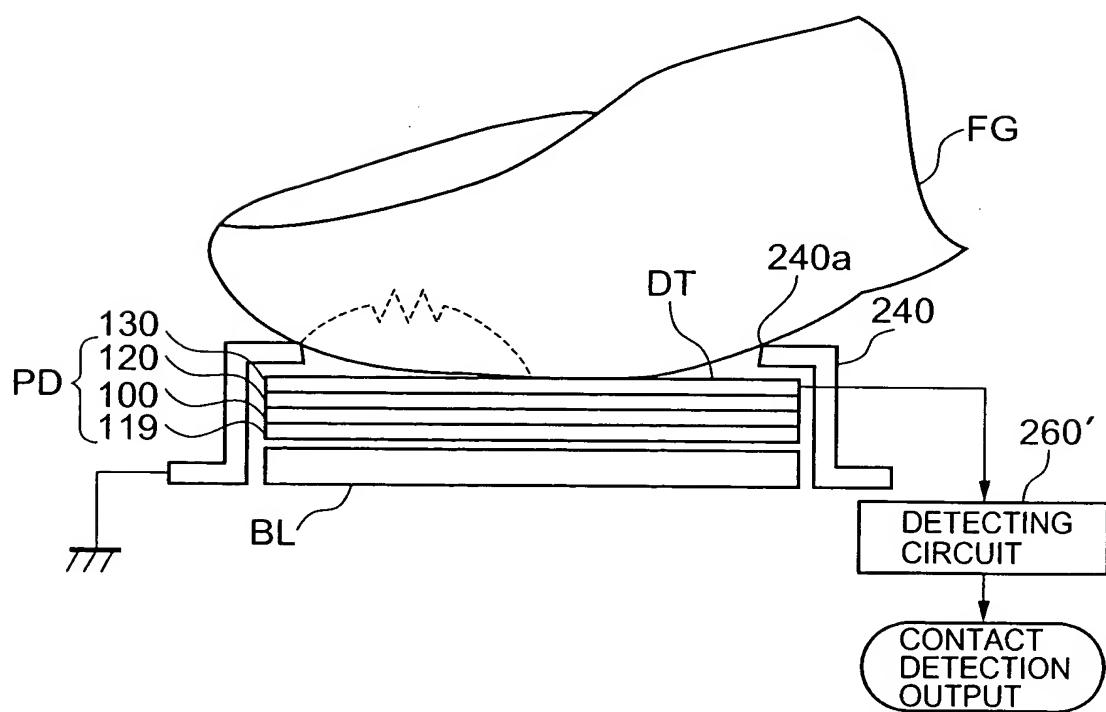


FIG. 17

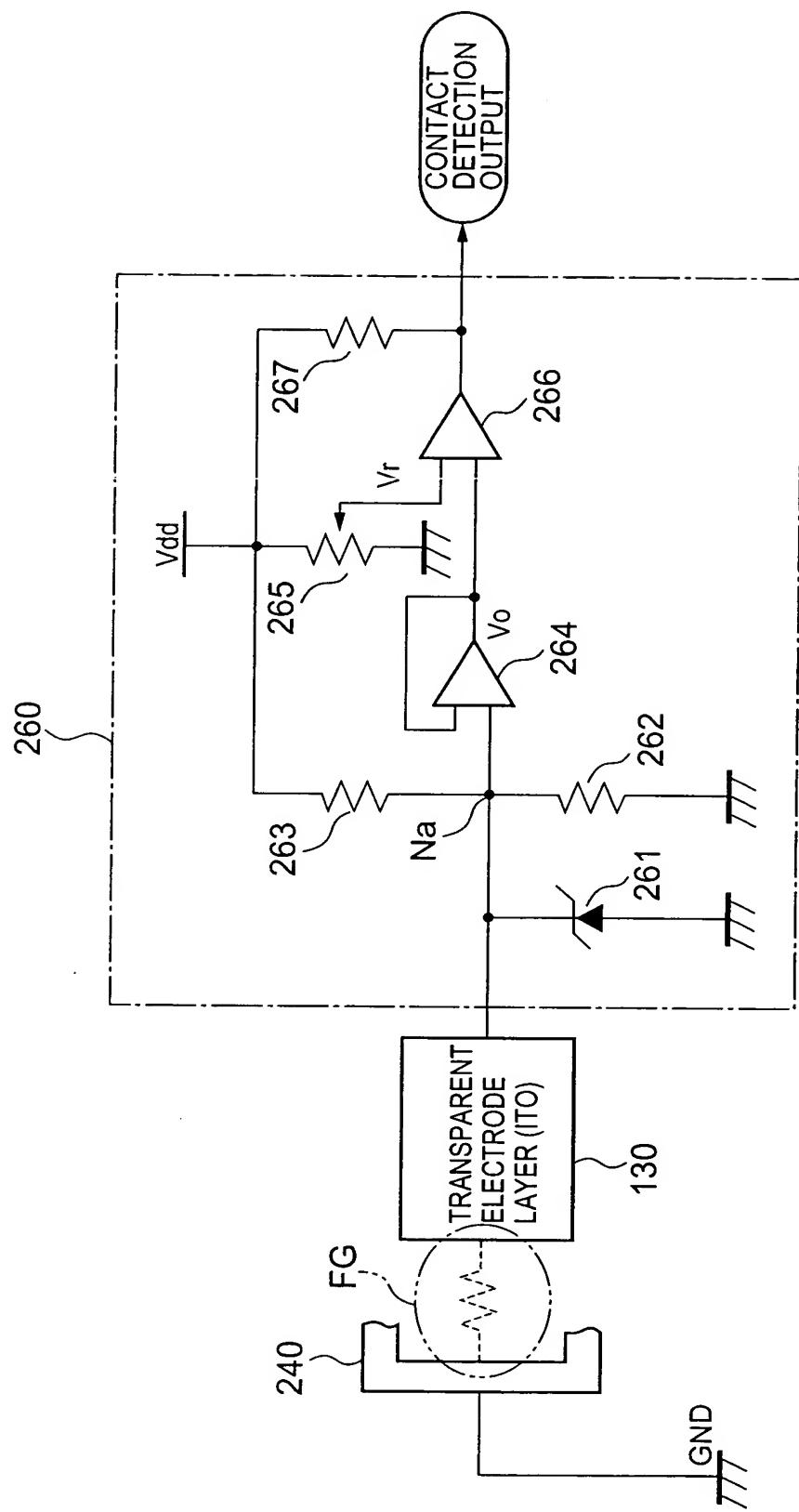


FIG. 18

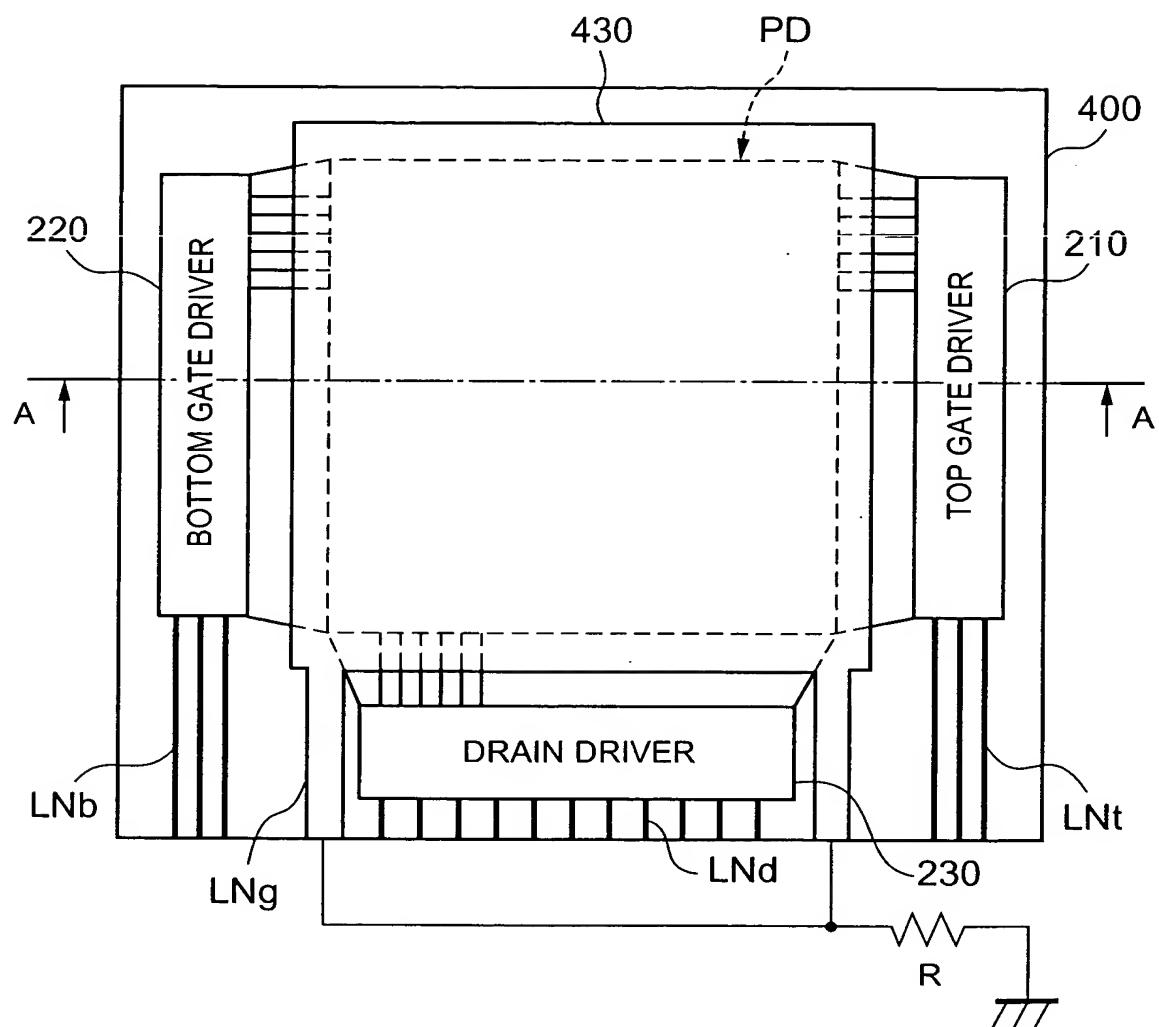


FIG. 19

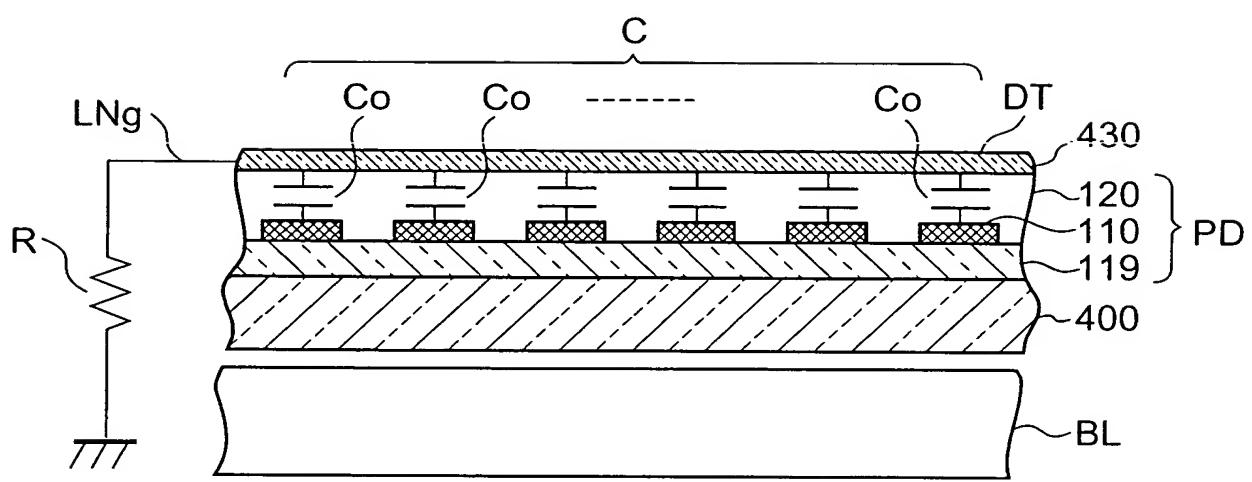
A-A PARTIAL CROSS SECTION

FIG. 20A

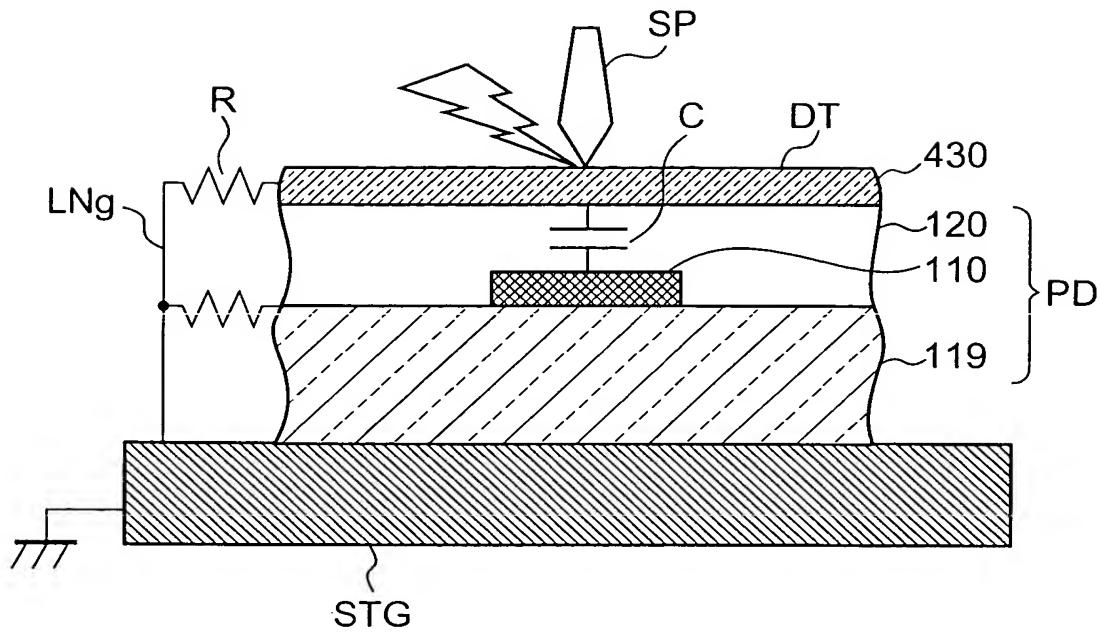


FIG. 20B

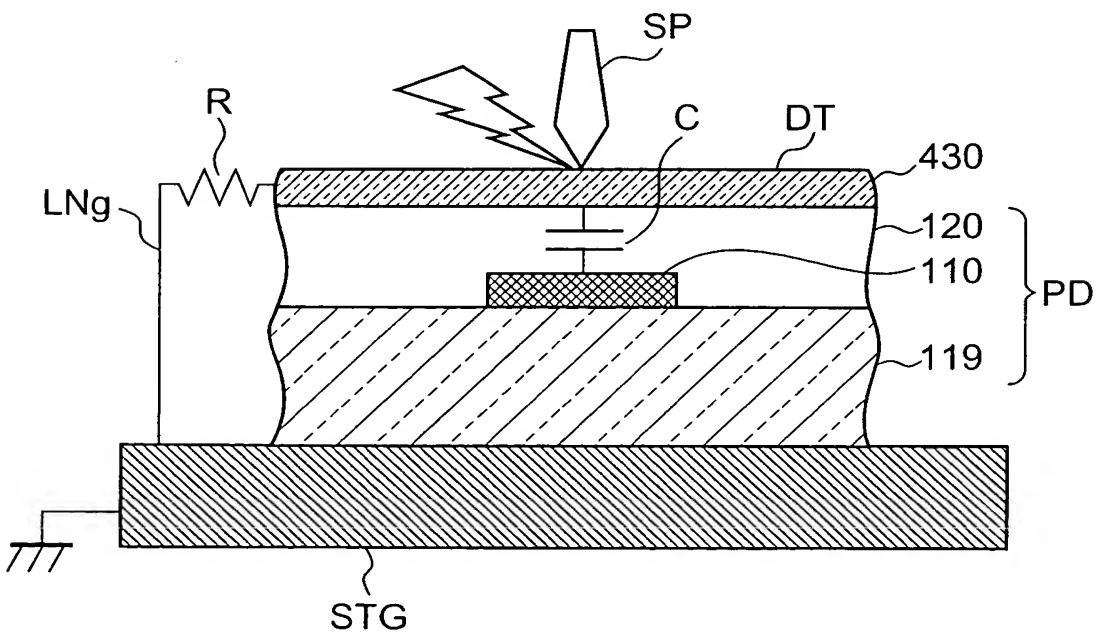


FIG. 21

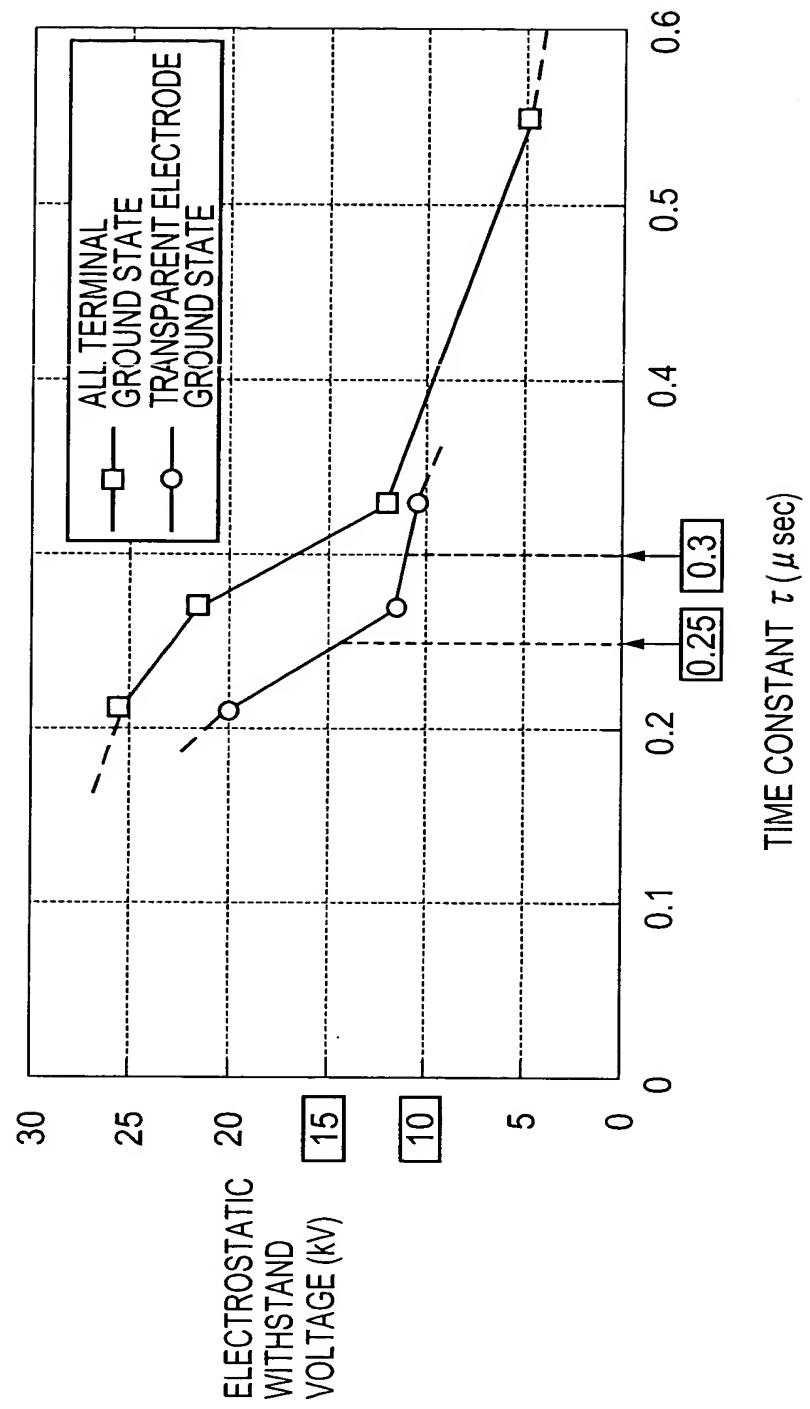


FIG. 22

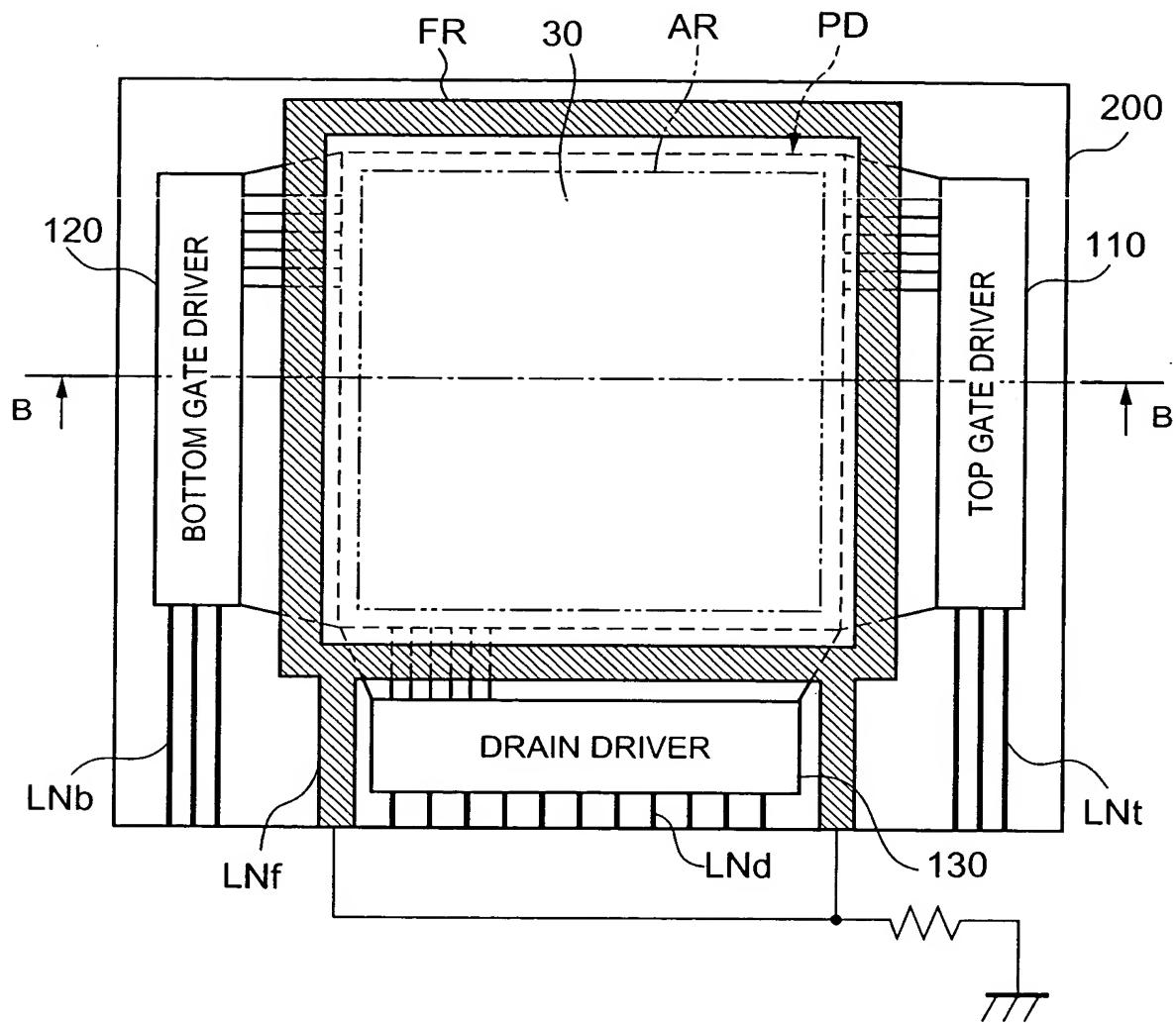
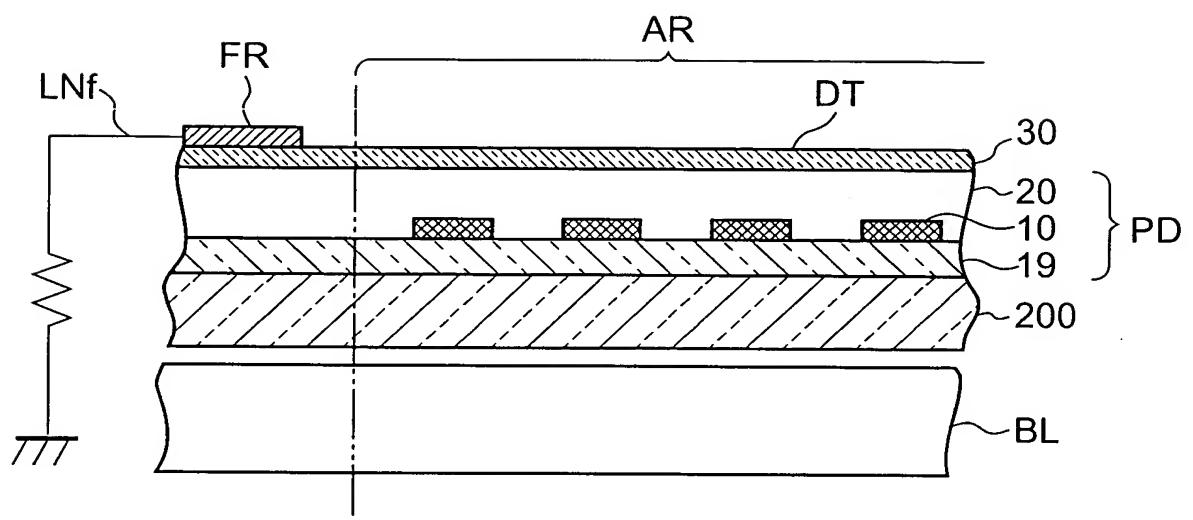


FIG. 23



B-B PARTIAL CROSS SECTION

FIG. 24A

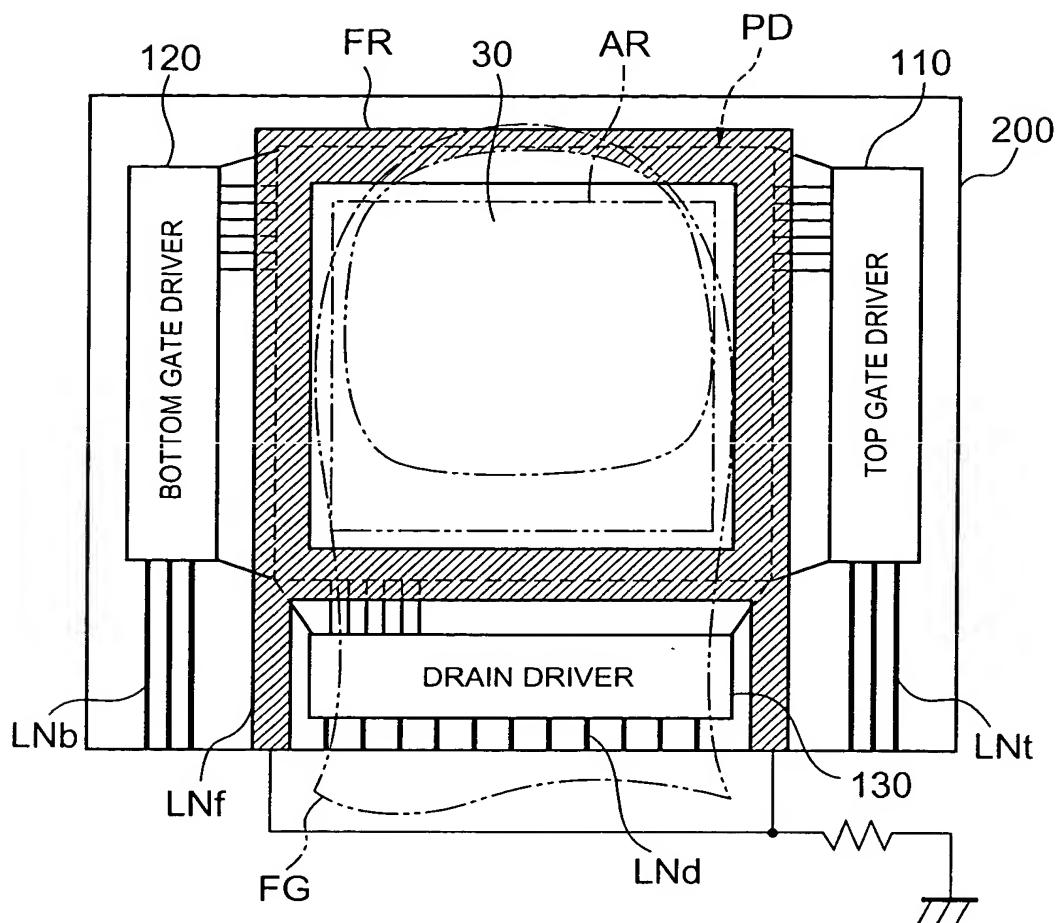


FIG. 24B

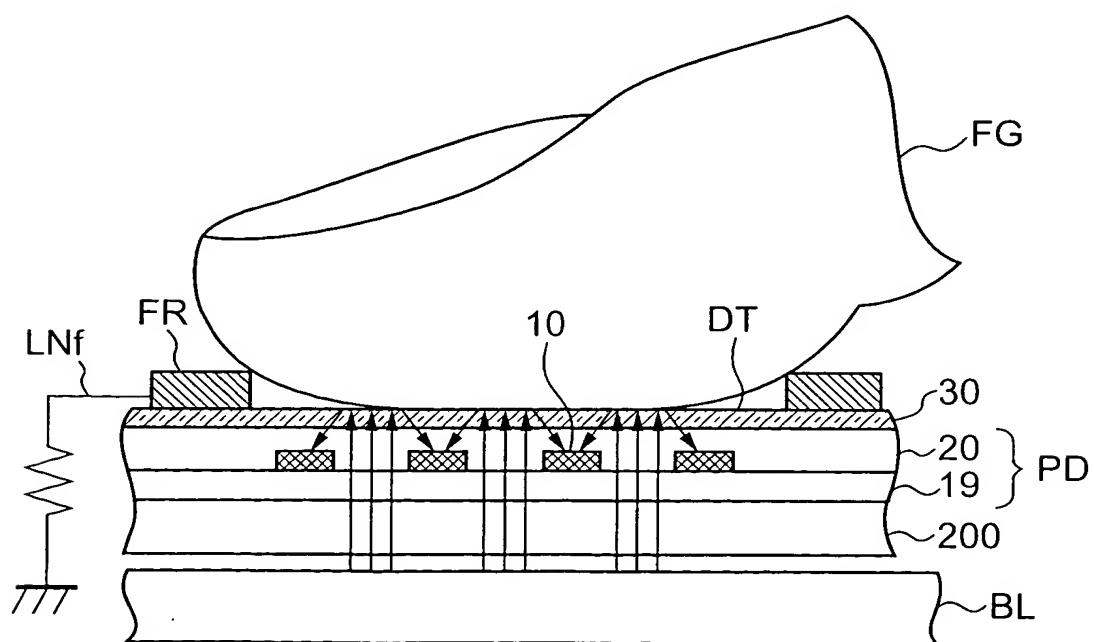


FIG. 25 PRIOR ART

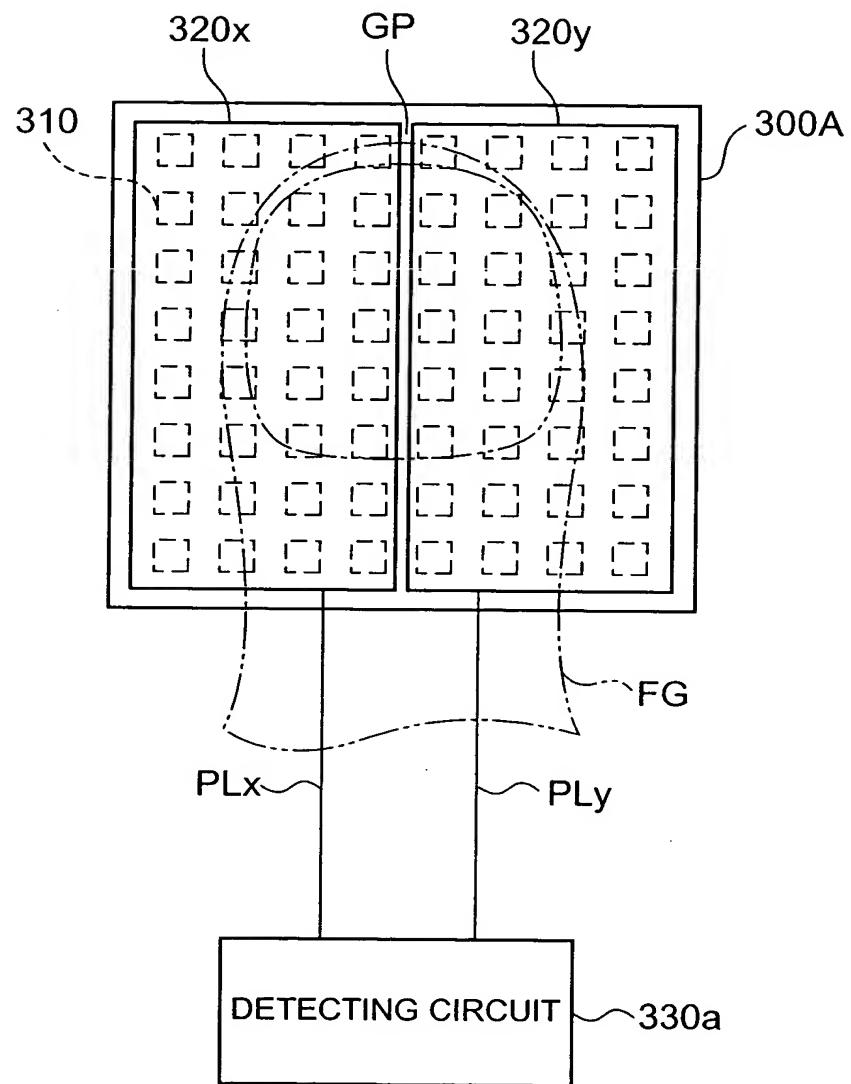


FIG. 26 PRIOR ART

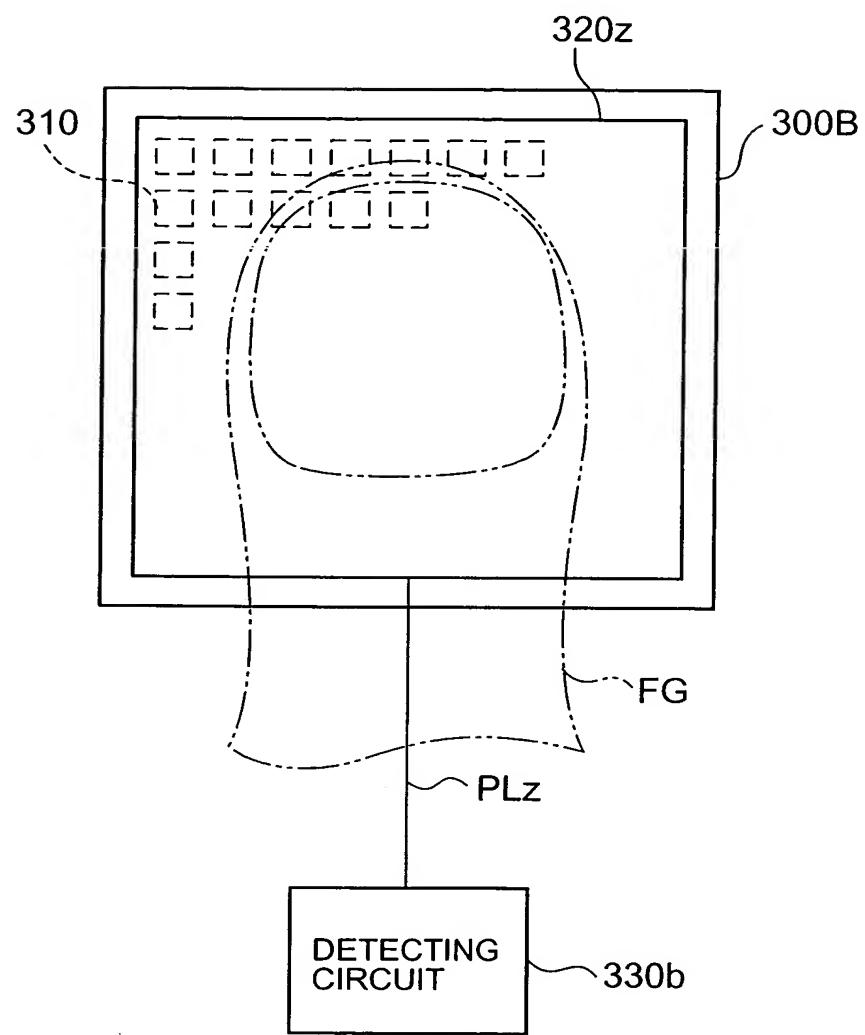


FIG. 27A PRIOR ART

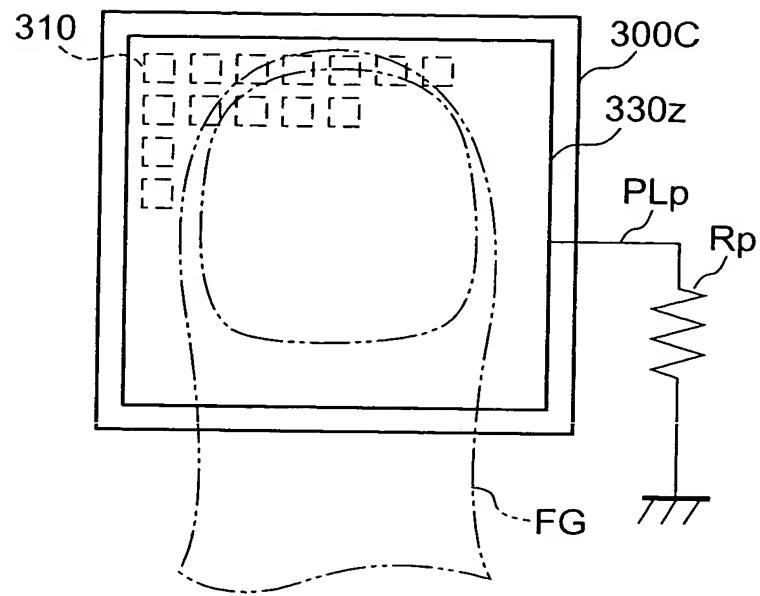


FIG. 27B PRIOR ART

